

IN THE SPECIFICATION

Amendments to the Specification:

Please replace the paragraph beginning page 10, line 7, with the following rewritten paragraph:

--Backing Register File 300 is added to create more local register storage, while not increasing the size of existing Register Files 308 and 310 as compared to Register Files 200 and 202 in FIG. 2. As shown in Figure 3, there is no direct connection between Backing Register File 300 and Execution Units 208, 210, 212, and 214. Thus, the complexity of Bypass Circuits 312, 314, like those mentioned in the Background section above, is not affected by the presence of Backing Register File 300 and Backing Register File 300 is therefore inaccessible to the execution units. Connection 302 is a full set of address and data lines, allowing Backing Register File 300 the ability to address and access individual registers in each of the Register Files 308 and 310. It will also be the case that the same connectivity will be present between Baking Register File 300 and any register files implemented in a particular processor.--